

FIG. 1

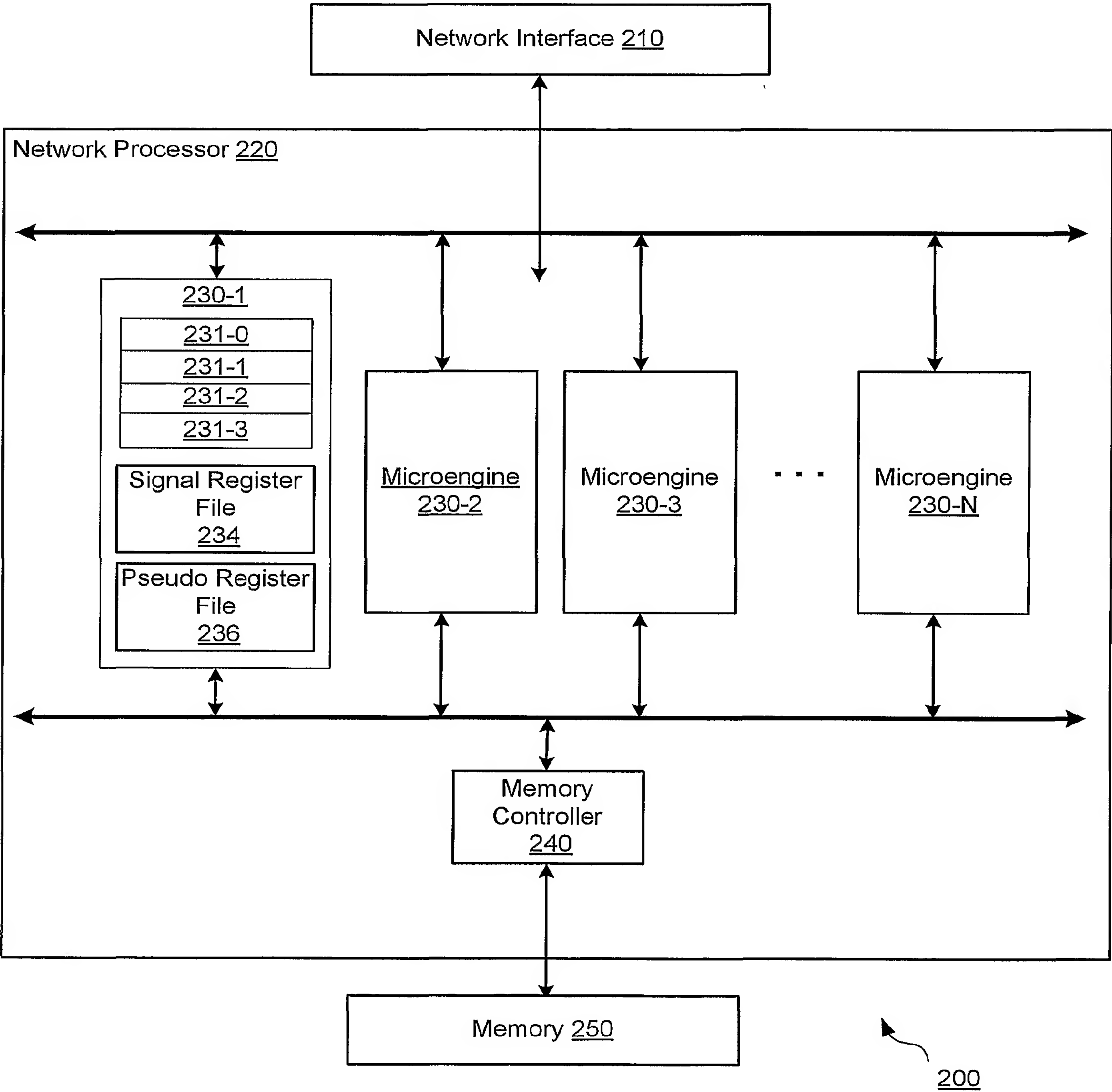
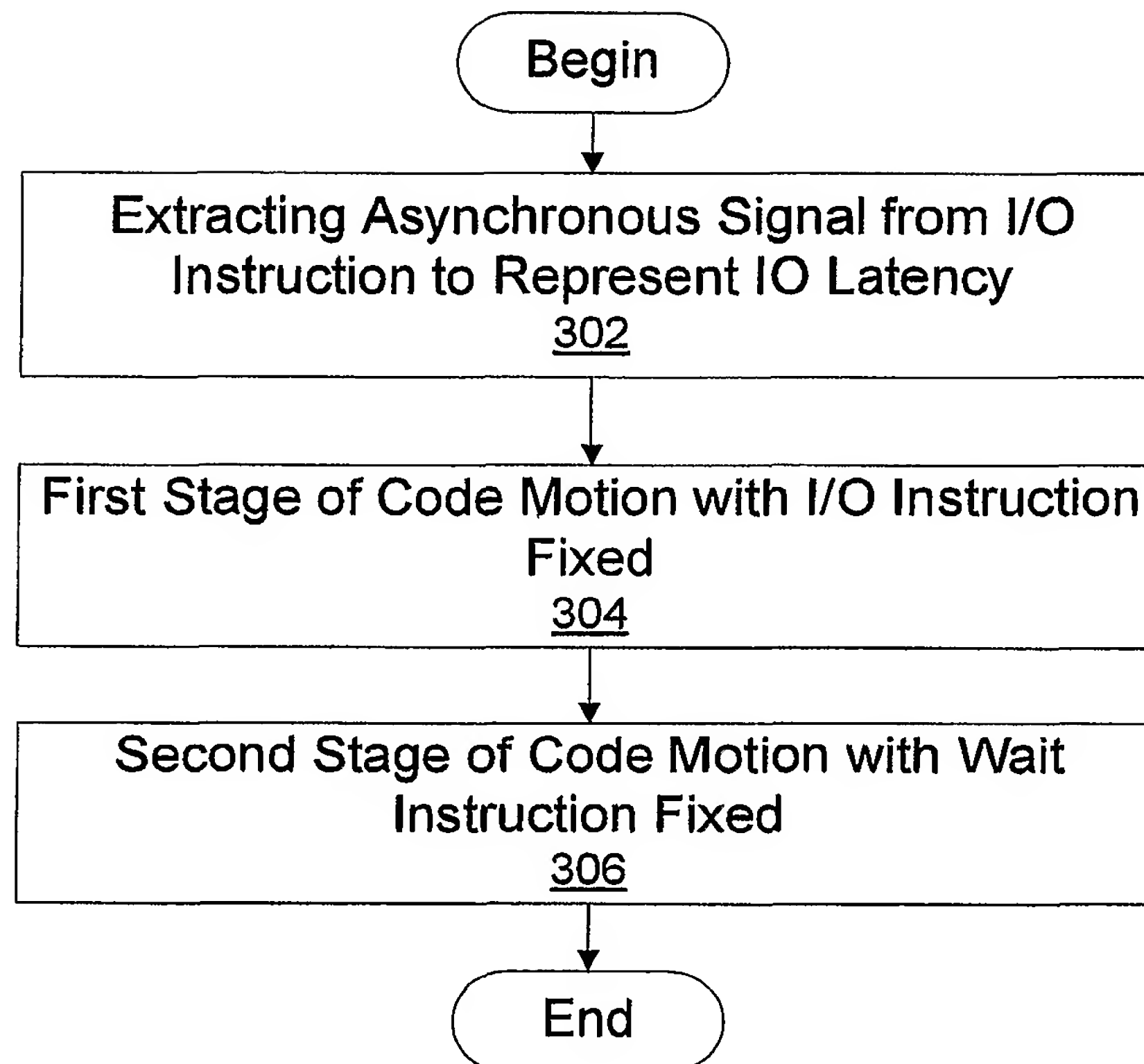


FIG. 2

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**FIG. 3**

[R2]=store R1 412
R3=R2+1
... ..
R4=R3<<2
R1=R5-R4

400

FIG. 4A

[R2]=store R1, signal s 422
Wait s 424
R3=R2+1
... ..
R4=R3<<2 426
R1=R5-R4

FIG. 4B

[R2]=store R1, signal s
↓
Wait s
↓
R1=R5-R4 436

FIG. 4C

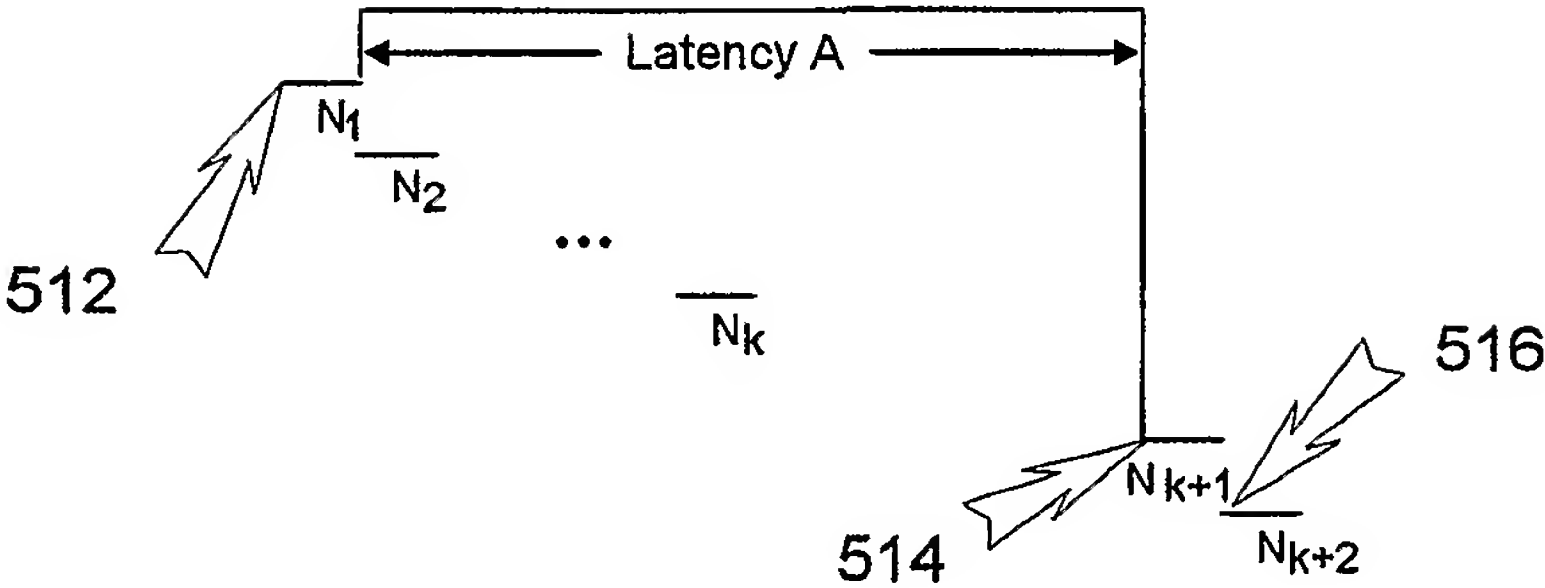


FIG. 5A

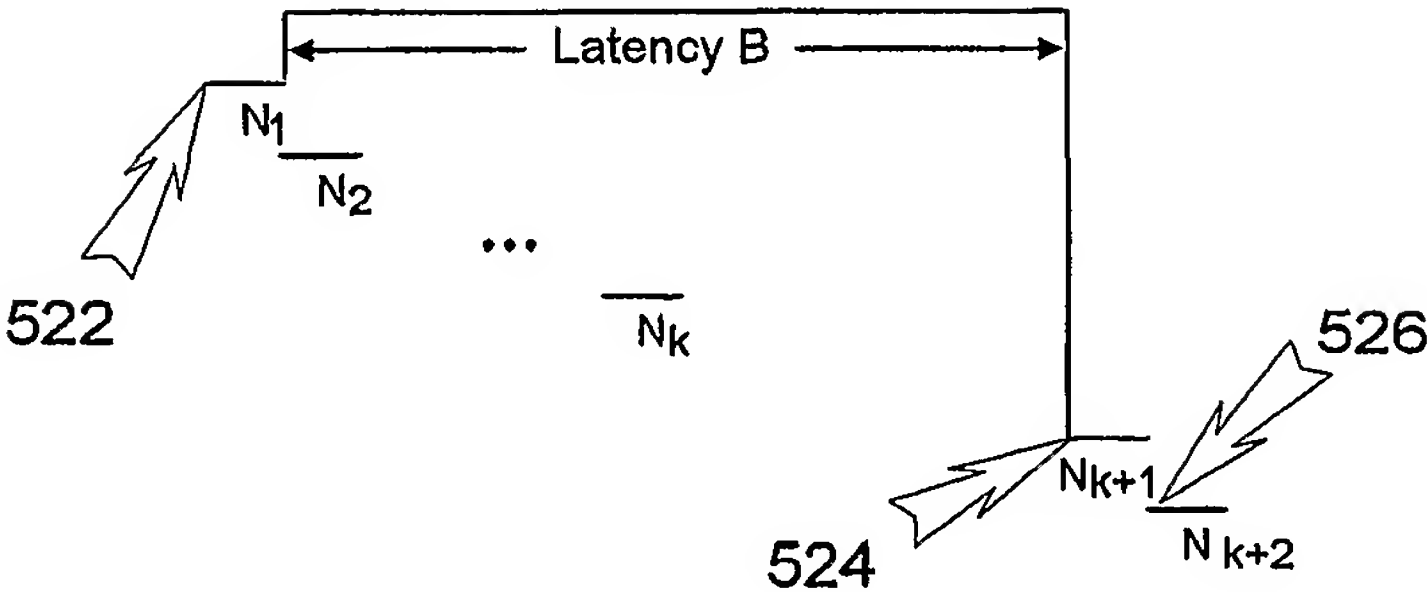


FIG. 5B

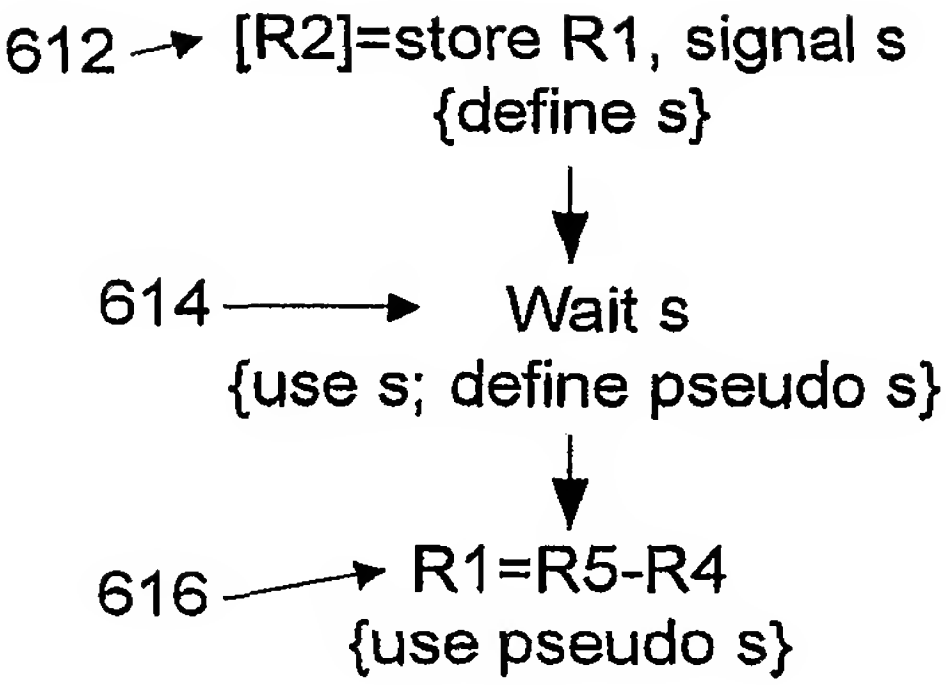


FIG. 6A

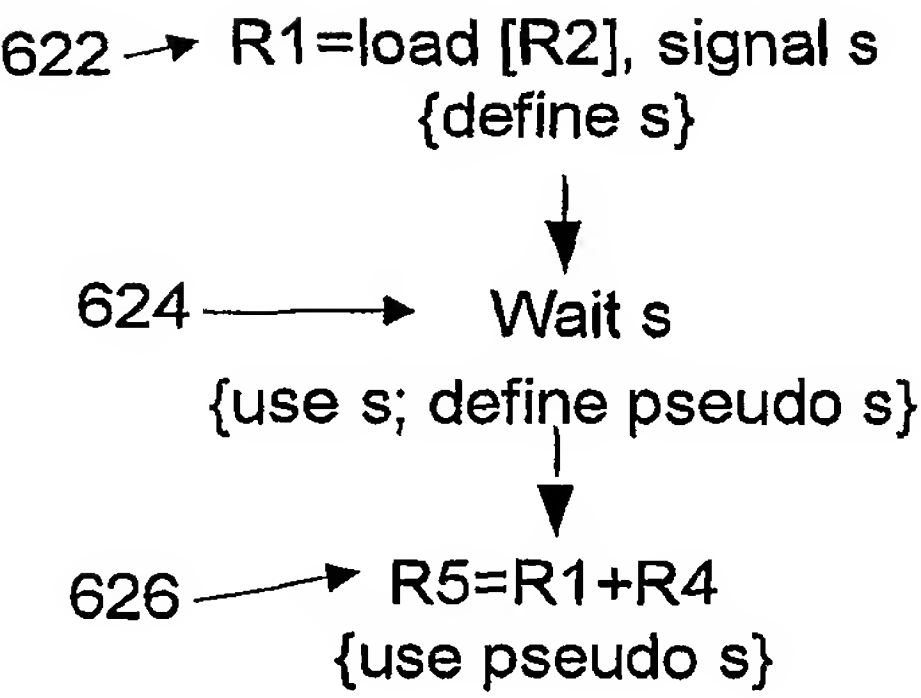


FIG. 6B

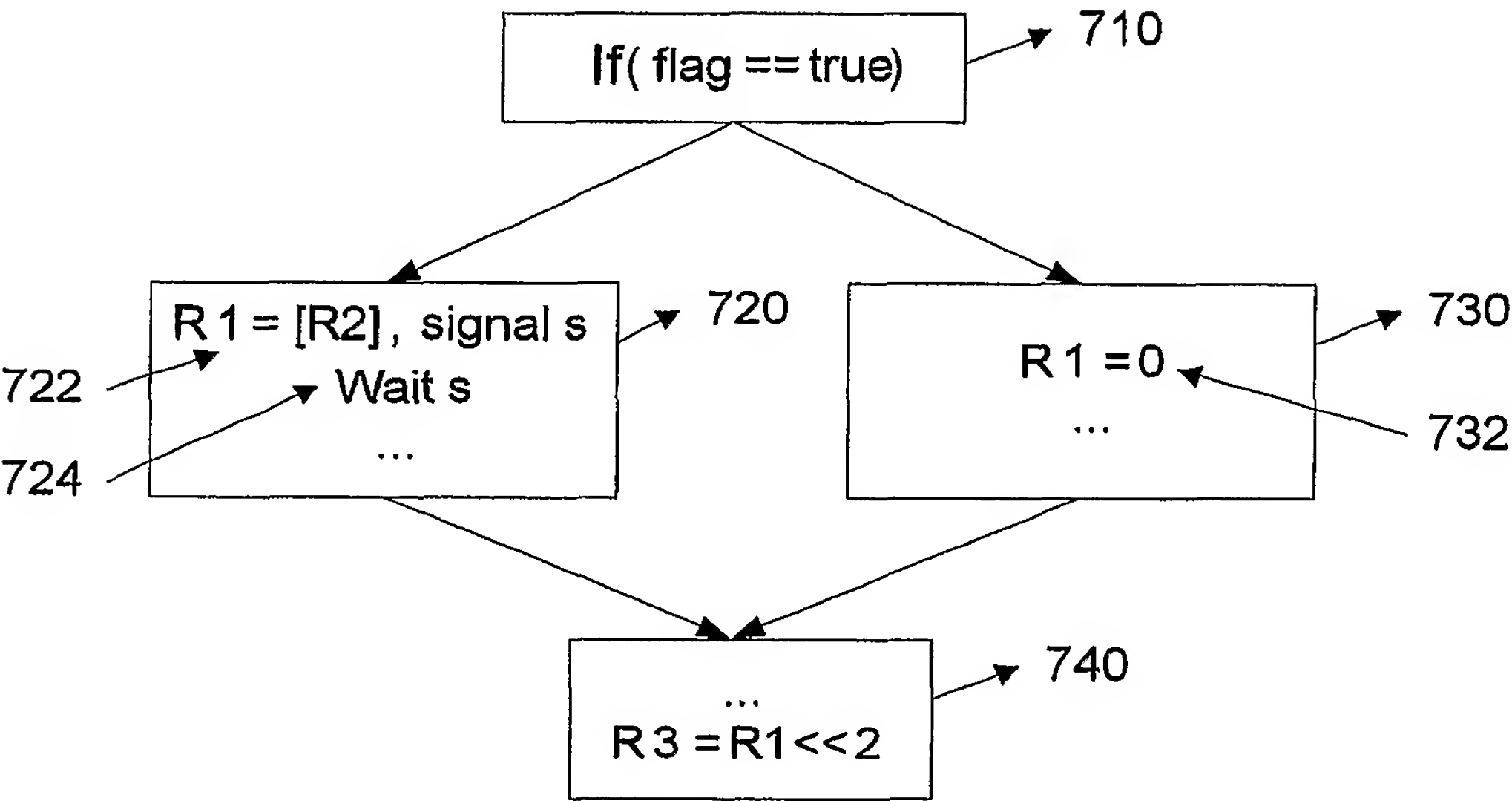


FIG. 7A

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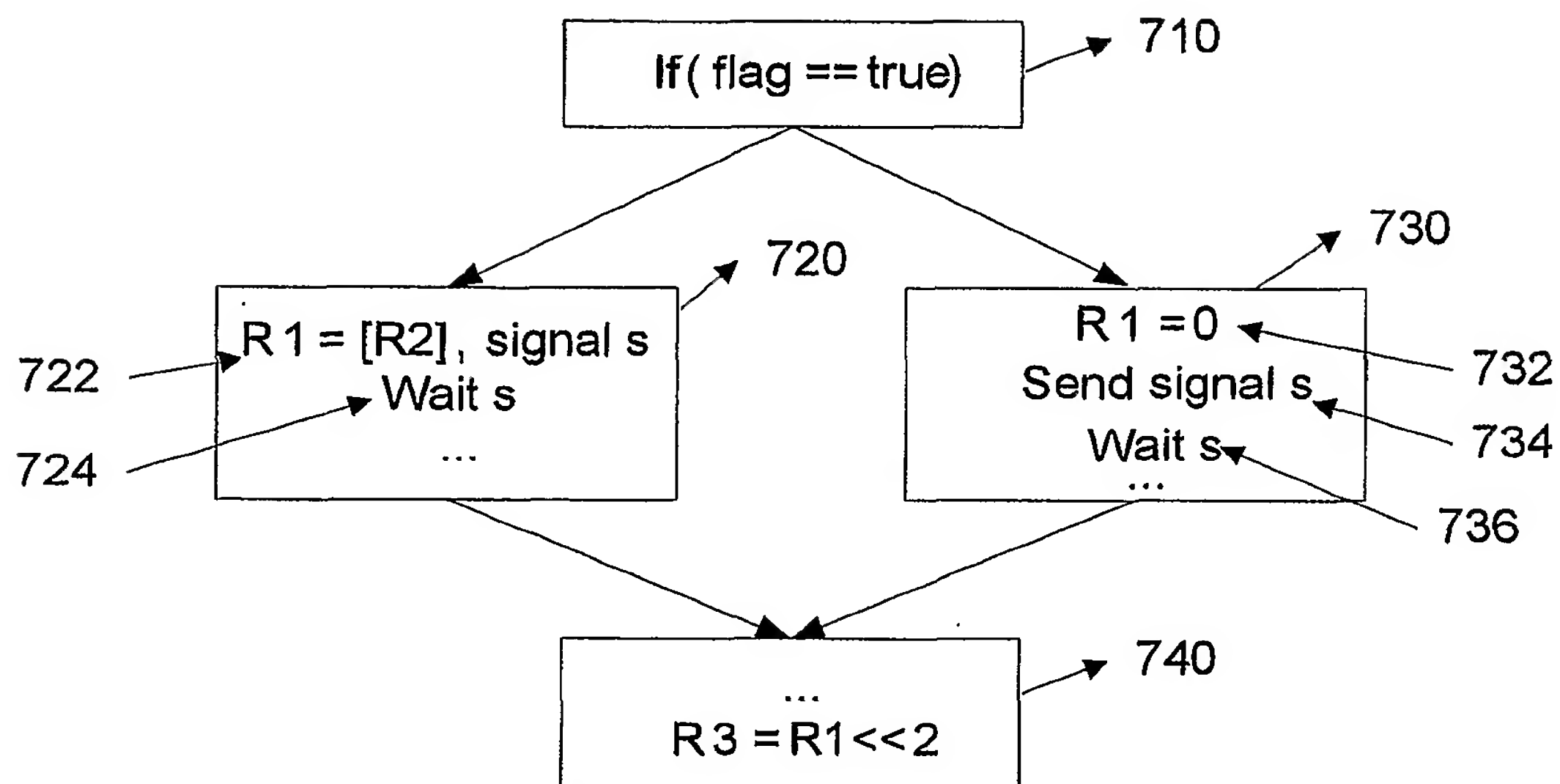


FIG. 7B

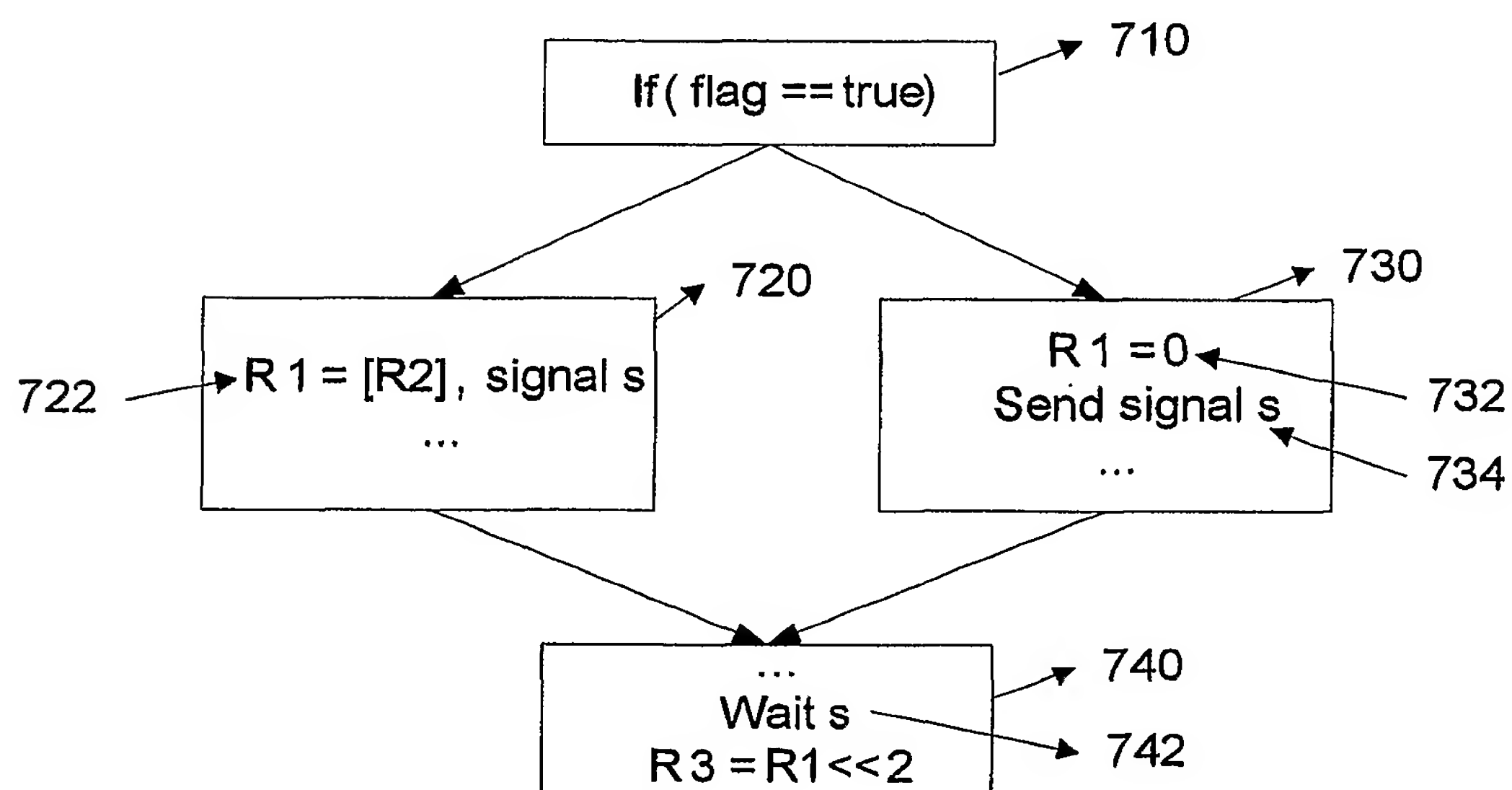


FIG. 7C

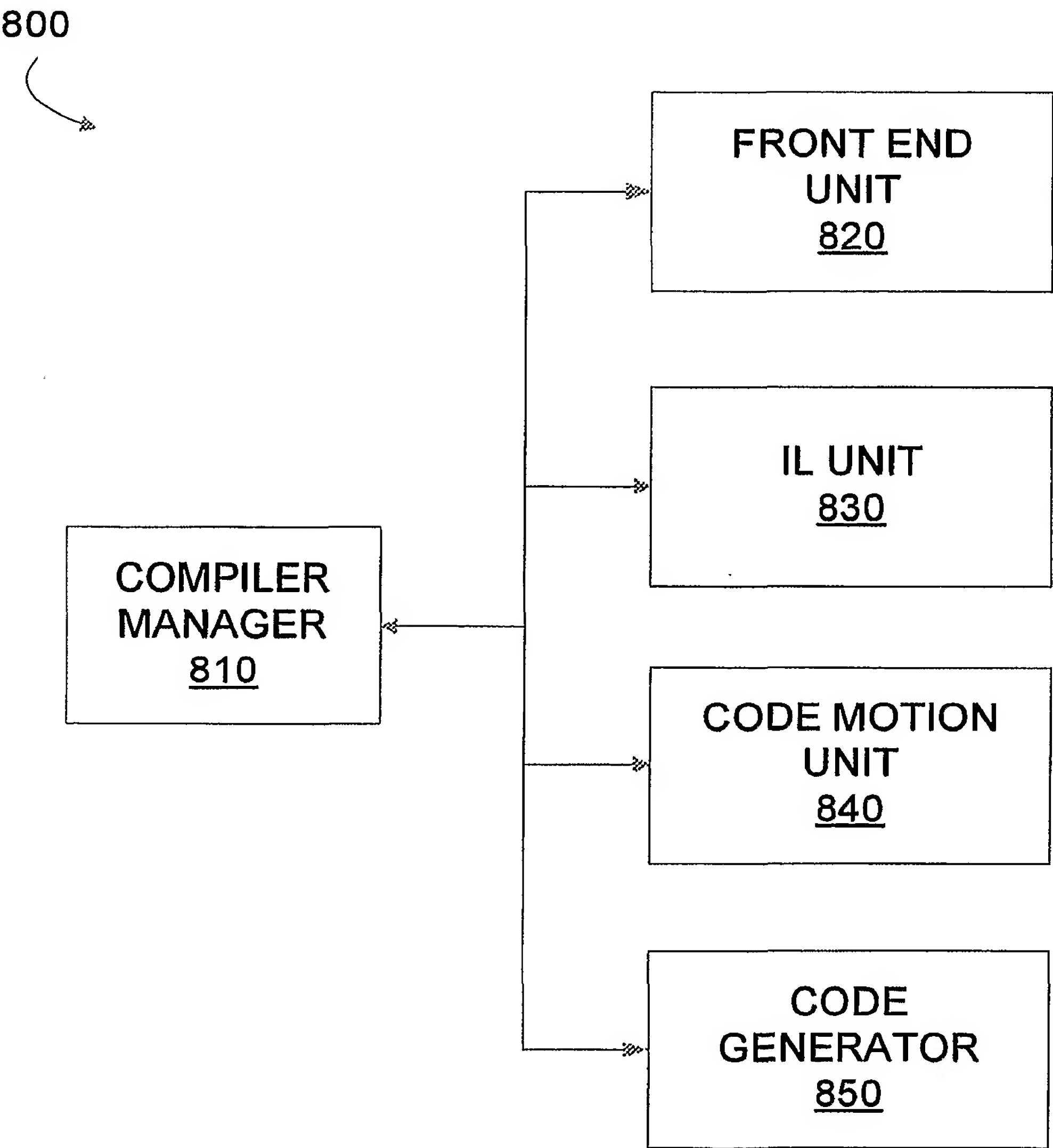


FIG. 8